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		Application Number	Not yet Assigned		
		Filing Date	January 14, 2002		
		First Named Inventor	Tomoyuki SASAKI et al		
		Group Art Unit	2814		
		Examiner Name	S. Rao		
Sheet	2	of	2	Attorney Docket Number	740819-732

3932 U.S. PTO
10/043257
01/14/02

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
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gm		S. Wolf and R.N. Tauber, Silicon Processing for the VLSI Era, Vol. L pp. 555-556, 569 and 575	

Examiner Signature		Date Considered	04/02/03
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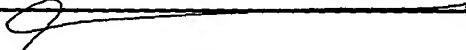
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U.S. PATENT DOCUMENTS						
Examiner Initials ¹	Cited No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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SU ↓ 8 ✓		JP	10-065000 A			03-06-1998		English Abstract
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